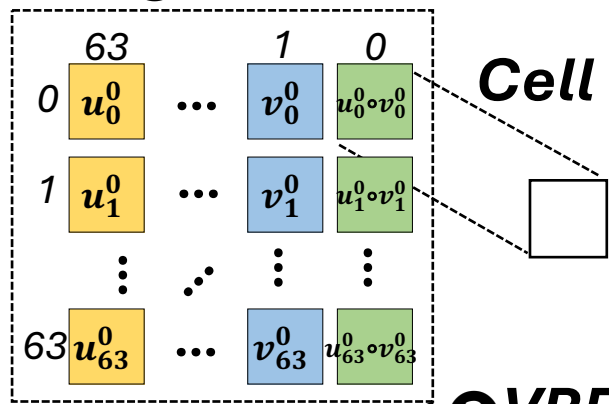
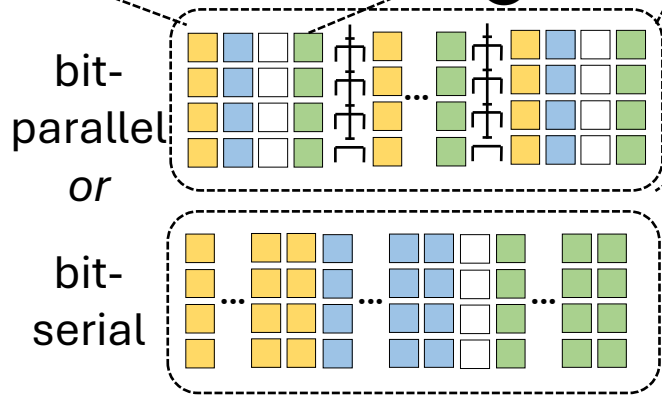


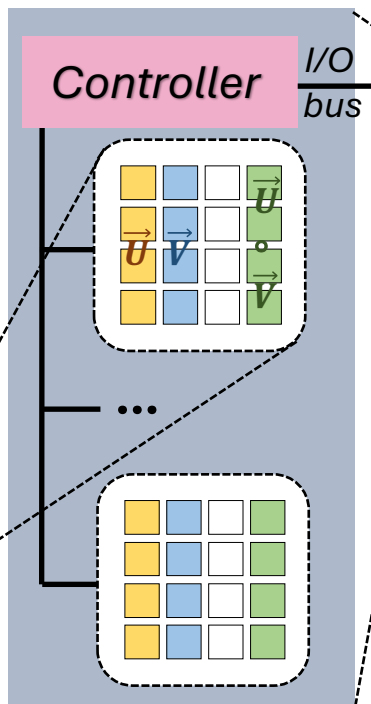
1 Tile



2 VRF



3 Node



Host CPU

extra storage

insts

data

4 Chip

